

AMENDMENTS TO THE CLAIMS

The following listing of claims replaces all prior listings of claims in this application.

1. (Currently Amended) A group write slave comprising:

an identification stage having a first ID input providing slave information, a second ID input providing group information, and an ID output, wherein said identification stage includes ID logic circuitry for processing comparing said first ID input and said second ID input;

a select stage having a select input and a select output, wherein said select stage includes select logic circuitry for processing said select input to detect group write identification information; and

a slave module, wherein said slave module includes a plurality of slave inputs communicated with said select output and said ID output, a plurality of slave outputs and slave logic circuitry for processing said plurality of slave inputs so as to create said plurality of slave outputs.

2. (Original) A group write slave according to claim 1, wherein said ID logic circuitry includes an ID comparator and an ID AND gate, wherein said ID comparator is communicated with said first ID input, said second ID input and said ID AND gate, and wherein said ID AND gate is communicated with said first ID input and said slave module.

3. (Original) A group write slave according to claim 2, wherein said select logic circuitry includes a group write comparator, an individual select comparator, an OR gate and a select AND gate, wherein said group write comparator and said individual select comparator are communicated with said select input and said OR gate and wherein said select AND gate is communicated with said OR gate, said select input, said ID comparator and said slave module.

4. (Original) A group write slave according to claim 2, wherein said ID comparator includes ID comparator logic circuitry which compares said first ID input with said second ID input.

5. (Original) A group write slave according to claim 3, wherein said group write comparator includes group write logic circuitry for processing said select input so as to determine whether said group write slave has been selected as a group.

6. (Original) A group write slave according to claim 3, wherein said individual select comparator includes individual select logic circuitry for processing said select input so as to determine whether said group write slave has been selected as an individual.

7. (Original) A group write slave according to claim 1, wherein said first ID input is communicated with a master so as to allow communication of a first master signal, wherein said first master signal includes slave address information and slave ID information.

8. (Original) A group write slave according to claim 1, wherein said second ID input is communicated with a master so as to allow communication of a second master signal, wherein said second master signal includes group write identification information.

9. (Original) A group write slave according to claim 1, wherein said select input is communicated with a master so as to allow communication of a third master signal, wherein said third master signal includes group write command information.

10. (Original) A group write slave according to claim 1, wherein at least one of said plurality of slave outputs is communicated with a type one status bus.

11. (Original) A group write slave according to claim 1, wherein at least one of said plurality of slave outputs is communicated with a type two status bus.

12. (Original) A group write slave according to claim 1, wherein at least one of said plurality of slave outputs includes a slave wait signal and a slave re Arbitrate signal and is communicated with a type three status bus.

13. (Original) A group write slave according to claim 1, wherein at least one of said plurality of slave outputs is responsive to a group wait signal, wherein said group wait signal is generated through a logical operation conducted external to said group write slave.

14. (Original) A group write slave design according to claim 1, wherein at least one of said plurality of slave outputs is responsive to a group rearbtrate signal, wherein said group rearbtrate signal is generated through a logical operation conducted external to said group write slave.

15. - 30. (Canceled)